Interference Cearch

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L43	1	(processor WITH (mod modular) WITH descriptor).CLM.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 22:13
L44	2	(processor WITH interleav\$3 WITH descriptor).CLM.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 22:13
L45	13	("false sharing").CLM.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 22:16



Updale	Search
J. 4	

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	5	(processor WITH (mod modular) WITH descriptor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR ·	ON	2006/10/11 22:06
L2	5	(processor WITH interleav\$3 WITH descriptor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 22:06
L3	3754	(711/154,157,5).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/11 22:09
L4	48555	("709").CLAS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/11 22:09
L5	159	L3 AND L4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR ·	ON	2006/10/11 22:09
L6	126	("false sharing")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 22:09
L7	6	L3 AND L6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 22:09

L8	1517	(711/157,5).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/11 22:09
L9	1211	(711/5).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/11 22:09
L10	439	(711/157).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF ·	2006/10/11 22:09
L11	44399	(multi-process\$3 (multiple NEAR2 processors))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 22:09
L12	190	L11 AND L8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 22:09
L13	333	("descriptor ring")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR ·	ON	2006/10/11 22:09
L14	92	L13 AND L11	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 22:09
L15	1	L14 AND L8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 22:09

10/11/2006 10:11:33 PM Page 2
C:\Documents and Settings\sgu\My Documents\EAST\Workspaces\10735113 Striping Across Multiple Cache Lines to Prevent False Sharir

110		112 410 10	LIC DCDLIC	00	ON	2006/10/11 22 55
L16	1	L13 AND L8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 22:09
L17	101877	(interleav\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 22:09
L18	90	L13 AND L17	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 22:09
L19	158	L13 AND (cache)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 22:09
L22	65	L13 AND (cacheline (cache ADJ line))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 22:09
L23	3754	(711/154,157,5).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/11 22:09
L24	48555	("709").CLAS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/11 22:09
L25	159	L23 AND L24	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 22:09

10/11/2006 10:11:33 PM Page 3
C:\Documents and Settings\sgu\My Documents\EAST\Workspaces\10735113 Striping Across Multiple Cache Lines to Prevent False Sharir

L26	126	("false sharing")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 22:09
L27	6	L23 AND L26	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 22:09
L28	1517	(711/157,5).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR ·	OFF	2006/10/11 22:09
L29	439	(711/157).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/11 22:09
L30	1211	(711/5).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/11 22:09
L31	44399	(multi-process\$3 (multiple NEAR2 processors))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 22:09
L32	. 190	L31 AND L28	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 22:09
L33	333	("descriptor ring")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 22:09

10/11/2006 10:11:33 PM Page 4

			T.10 200-1-	00	011	2006/40/44 22 55
L34	92	L33 AND L31	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 22:09
L35	1	L34 AND L28	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 22:09
L36	1	L33 AND L28	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 22:09
L37	101877	(interleav\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 22:09
L38	90	L33 AND L37	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 22:09
L39	158	L33 AND (cache)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 22:09
L40	5	(US-20020174252-\$).did. or (US-6981122-\$ or US-6947438-\$ or US-6912602-\$ or US-6831892-\$ or US-6691178-\$ or US-6748479-\$ or US-6889278-\$ or US-6526451-\$). did.	US-PGPUB; USPAT	OR	ON	2006/10/11 22:09
L41	1	L40 AND (cacheline (cache ADJ line))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 22:11

10/11/2006 10:11:33 PM Page 5
C:\Documents and Settings\sgu\My Documents\EAST\Workspaces\10735113 Striping Across Multiple Cache Lines to Prevent False Sharir

L42	65	L33 AND (cacheline (cache ADJ line))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT;	OR	ON	2006/10/11 22:09
			IBM_TDB			

10/11/2006 10:11:33 PM Page 6
C:\Documents and Settings\sgu\My Documents\EAST\Workspaces\10735113 Striping Across Multiple Cache Lines to Prevent False Sharir